



Department of Electrical and Electronic Engineering

Schedule of Term Final Examination (Modified), Spring-2017

Regular & Evening (Defense & Central Lab Viva)

Published Date: 08/04/2017

Building Code:

B1	Admin Building
B2	CSE Building
B3	EEE Building

Slot No.	A: Central Lab Viva (Only For Regular)		
Date	13/04/2017		
Day	Thursday		
Time	3:00 PM~5:00 PM		
Batch No.	Viva Board	Lab Room	Lab Assistant*
13 R(Mir)	Panel-05	PSP	*AAS
14 R(Mir)	Panel-05	PSP	*AAS
15 R(Mir)	Panel-08	VLSI	*MMR
16 R(Mir)	Panel-07	Embedded	*SR
17 R(Mir)	Panel-08	VLSI	*MMR
13 Regular	Panel-07	Embedded	*SR
14 Regular	Panel-07	Embedded	*SR
15 Regular	Panel-08	VLSI	*MMR
16 Regular	Panel-07	Embedded	*SR
17 Regular	Panel-08	VLSI	*MMR
18 Regular	Panel-06	Machine	*MMH
19 Regular	Panel-05	PSP	*AAS
20 Regular	Panel-04	Computer	*MAH
21 Regular	Panel-03	Electronics	*AH
22 Regular	Panel-02	Electrical	*MAA
23 Regular	Panel-02	Electrical	*MAA
24 Regular	Panel-01	Physics	*MSR

Slot No.	B: Thesis Progress Report (Regular & Evening)		
Date	28/04/2017		
Day	Friday		
Time	7:00 PM~9:00 PM		
Batch No.	Progress Report Board	Room No.	Lab Assistant*
14 Regular (Mir)	TBA	TBA	TBA
14 Regular (Main)	TBA	TBA	TBA
17 Eve (A)	TBA	TBA	TBA
17 Eve (BC)	TBA	TBA	TBA

#(TBA = To Be Announced)#

Slot No.	C: Thesis Pre-Defense (Regular & Evening)		
Date	29/04/2017		
Day	Saturday		
Time	7:00 PM~9:00 PM		
Batch No.	Pre-Defense Board	Room No.	Lab Assistant*
13 Regular (Mir)	TBA	TBA	TBA
16 Eve (Mir)	TBA	TBA	TBA
16 Eve (A)	TBA	TBA	TBA
16 Eve (B)	TBA	TBA	TBA
16 Eve (C)	TBA	TBA	TBA

#(TBA = To Be Announced)#

Prepared and Checked by:
The Exam Routine Committee
(Theory & Lab)
Dept. of EEE, UU, Dhaka-1230

Convener: Md. Moiful Alam (Senior Lecturer)
Members: Md. Hasin Reza Siddique (Senior Lecturer)
Md. Imrul Kayes (Senior Lecturer)
Vaskor Mostafa (Lecturer)

Approved by:
Dr. Md. Shakowat Zaman Sarker
Associate Professor & Chairman
Dept. of EEE, UU, Dhaka-1230